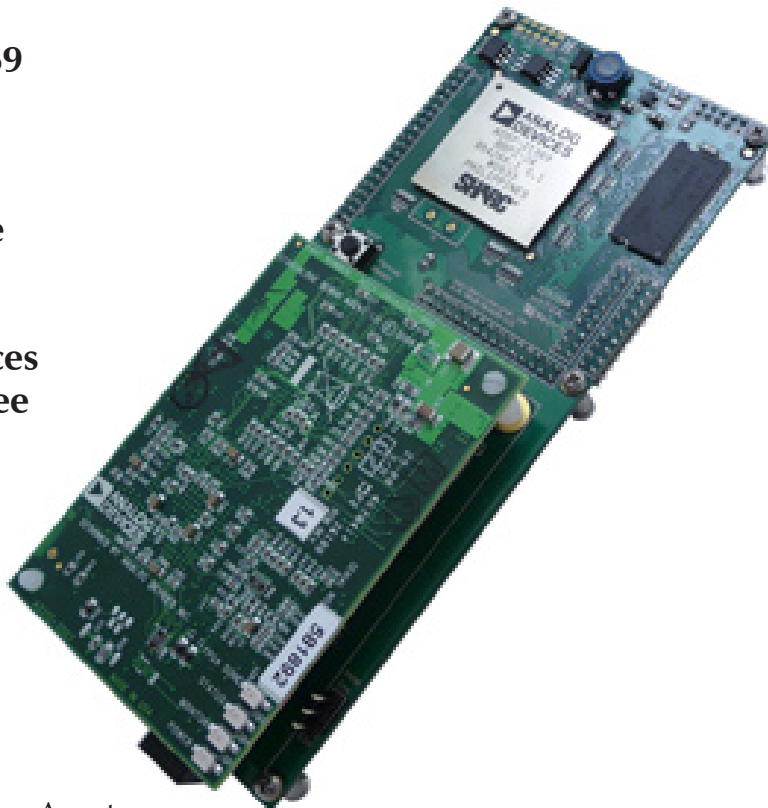


dspblok™ 21369zx with ICE

- **Analog Devices ADSP-21369 SHARC DSP**
- **Footprint Compatible Development Board for the dspblok 21369zx**
- **Supported by Analog Devices VisualDSP++ Including Free KIT License**
- **Includes Analog Devices EZ-KIT Lite Debugger**



The dspblok™ 21369zx with ICE combines a dspblok 21369zx with an Analog Devices Standalone Debug Agent.

When used in conjunction with Analog Devices VisualDSP++, you have a complete tool chain to develop applications that can run on a dspblok 21369zx. You can use a free, feature restricted, VisualDSP++ KIT license or take advantage of a FULL VisualDSP++ license.

Even though the dspblok 21369zx with ICE is larger than the production oriented dspblok 21369zx to accommodate the debugger, the connector footprint is identical. This allows you to develop code for your target and then substitute the smaller production oriented dspblok when you are finished. Even the power requirements are identical since the debugger portion of the dspblok 21369zx with ICE is powered independently.



Product Variations:

Standard – 400MHz, 64Mb SDRAM, Comm. Temp P/N A.08369A-ICE
400MHz, 128Mb SDRAM, Comm. Temp P/N A.08369A-1-ICE

Clock Options:

External Clocking (standard)
22.1184 MHz (append -22.1184 to part number)
24.576 MHz (append -24.576 to part number)
25.000 MHz (append -25.000 to part number)

Accessories:

5V, 1A Switching wall power supply, 100-130 VAC mains P/N TW.30687

Key Components:

Analog Devices ADSP-21369KBPZ-3A
Micron MT48LC2M32 or MT48LC4M32
Atmel AT26F004 4Mb Serial Flash
Microchip 25LC640 Serial EEPROM

Exposed I/O (3.3 V):

DAI 20 – DAI 1
DPI 14 – DPI 7, DPI 4, DPI 3 – DPI 1 (assigned as SCK, MISO & MOSI)
FLG1, FLG0
RESET# (Open Drain)
DSP CLKOUT
DATA BUS: D31-D0, A23-A0, RD#, WR#, ACK, MS3#-MS1#

Size:

60mm x 140mm
Footprint compatible with the dspblok 21369 section (60mm x 60 mm)

Connectors:

Dual row 2mm headers
5.5/2.5mm Coaxial power jack, center positive

Power Requirements:

3.3V – 400mA Largely dependent on core clock and computation load.
5.0V – 500mA Regulated (used for debugger section)



Danville Signal Processing, Inc.
38570 100th Avenue
Cannon Falls, MN 55009 USA

Web: www.danvillesignal.com
Email: dsp@danvillesignal.com
Telephone: 507-263-5854